



Docket No. RD-27,606

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: Group Art Unit No. 2131

ANDREW I. DEITSCH, ET AL

Serial No. 09/741,251

: Examiner

Filed December 21, 2000

: If Allowed--

For GATEWAY FOR SECURELY CONNECTING
ARBITRARY DEVICES AND SERVICE PROVIDERS

: Attn: Official Draftsman

: Allowed Date

: Issue Batch No.

To the Assistant Commissioner for Patents:

TRANSMITTAL OF FORMAL DRAWINGS

Enclosed please find 7 sheet(s) of formal drawings for the above-identified patent application.

Respectfully submitted,

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FIG. 1 is a block diagram of a computer system 10. The system 10 includes a processor 12, memory 14, and a data pathway 16. The processor 12 is connected to the data pathway 16. The memory 14 is connected to the data pathway 16. The data pathway 16 is connected to input devices 20, a display 22, a modem/network card 24, and a mass storage device 26. The input devices 20 include a keyboard 18 and a mouse 18.

FIG. 1

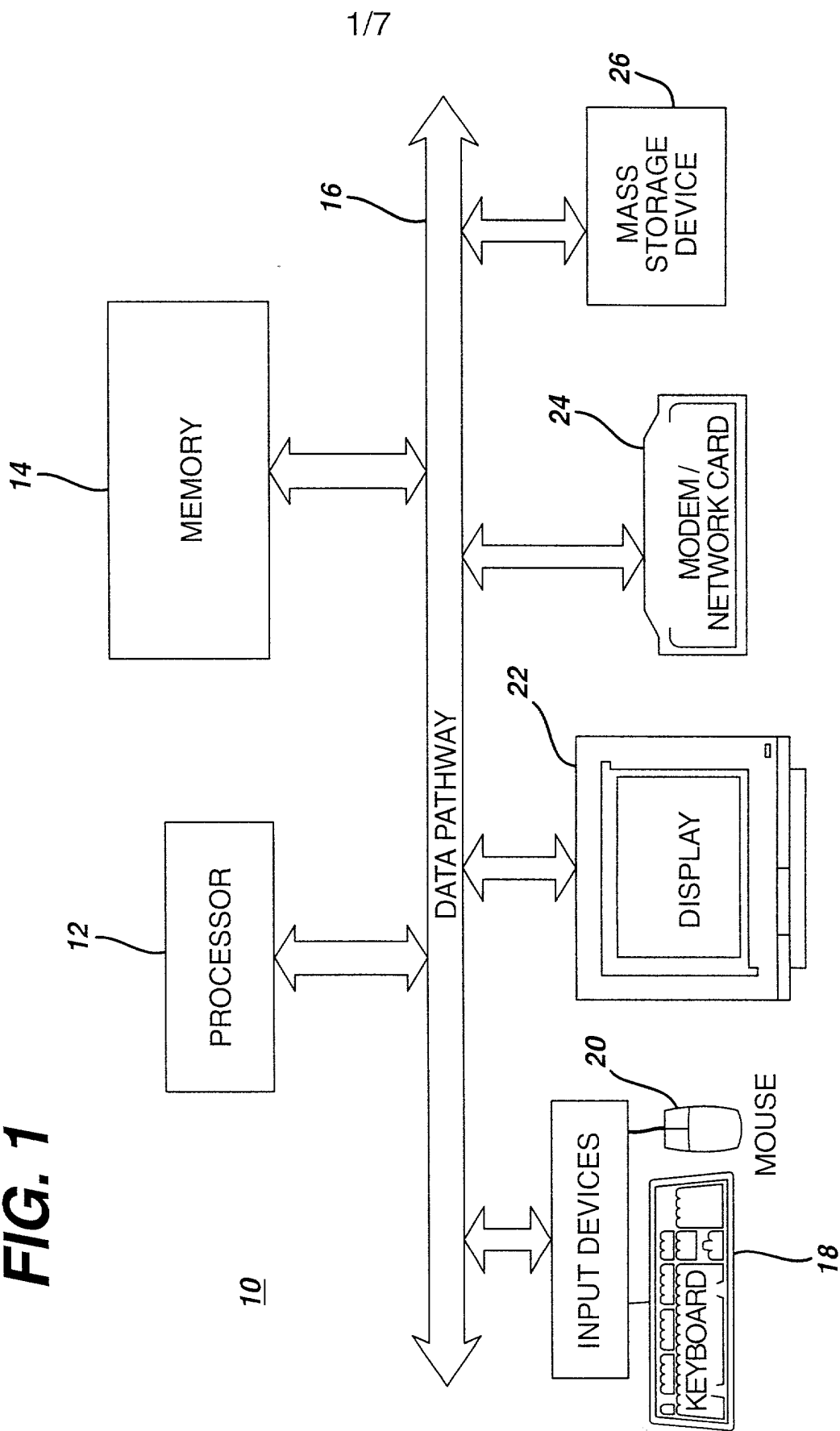


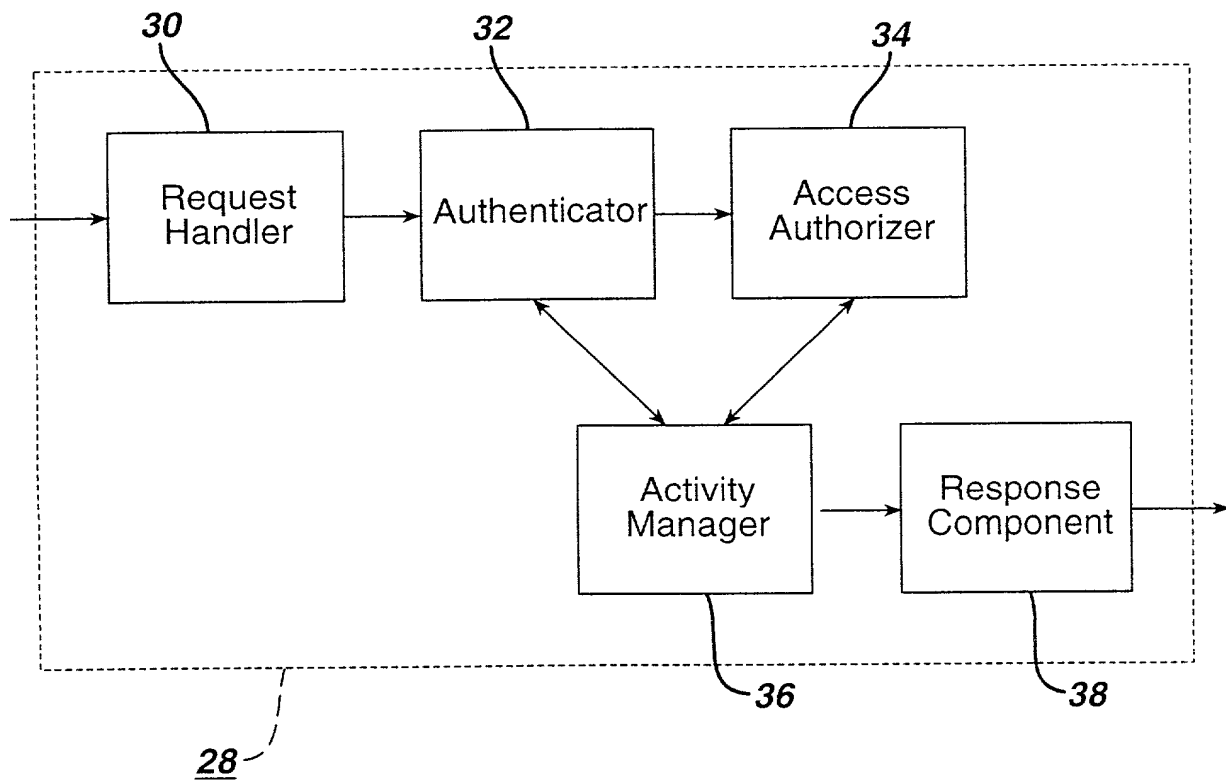
FIG. 2

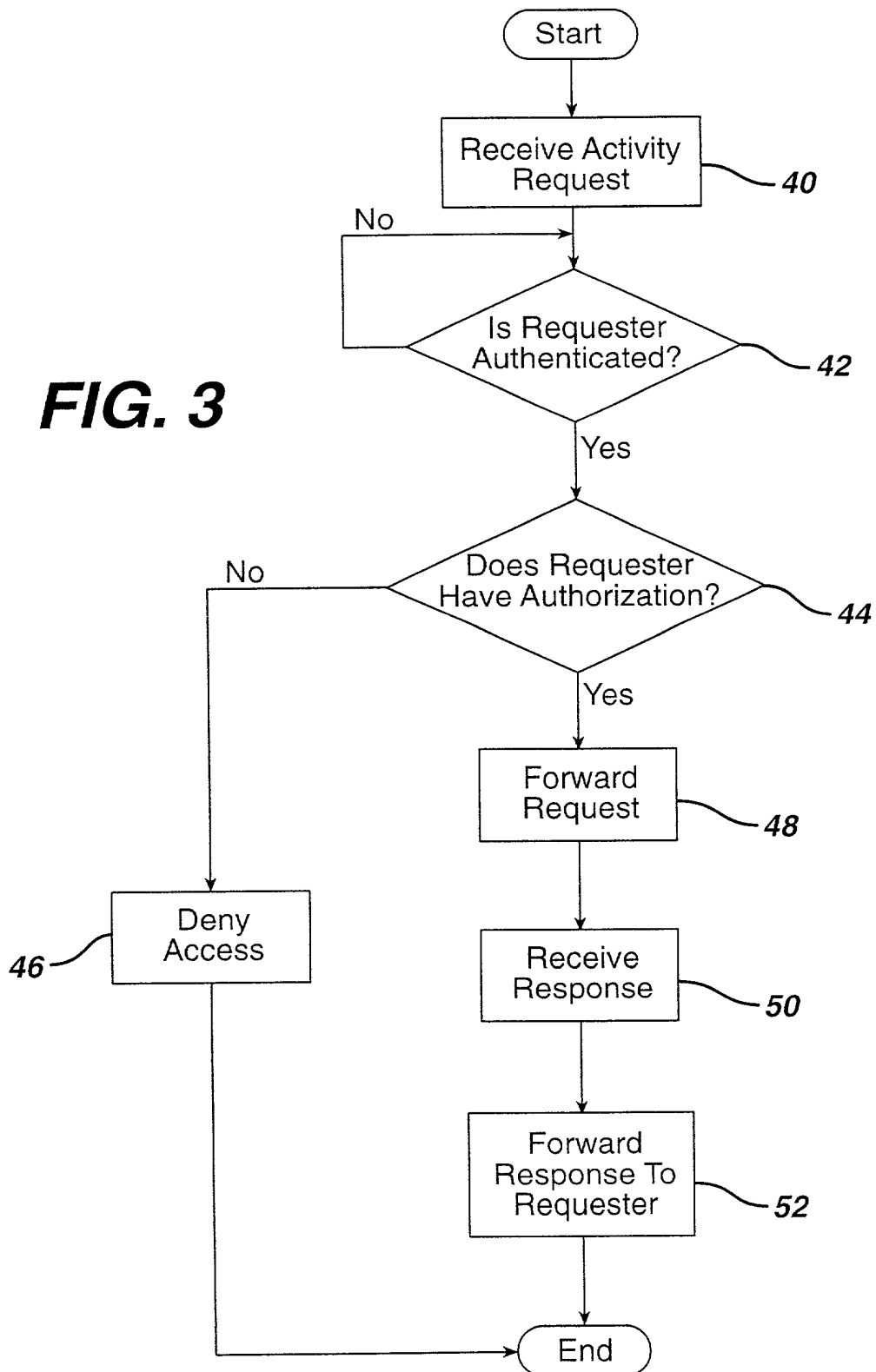
FIG. 3

FIG. 4

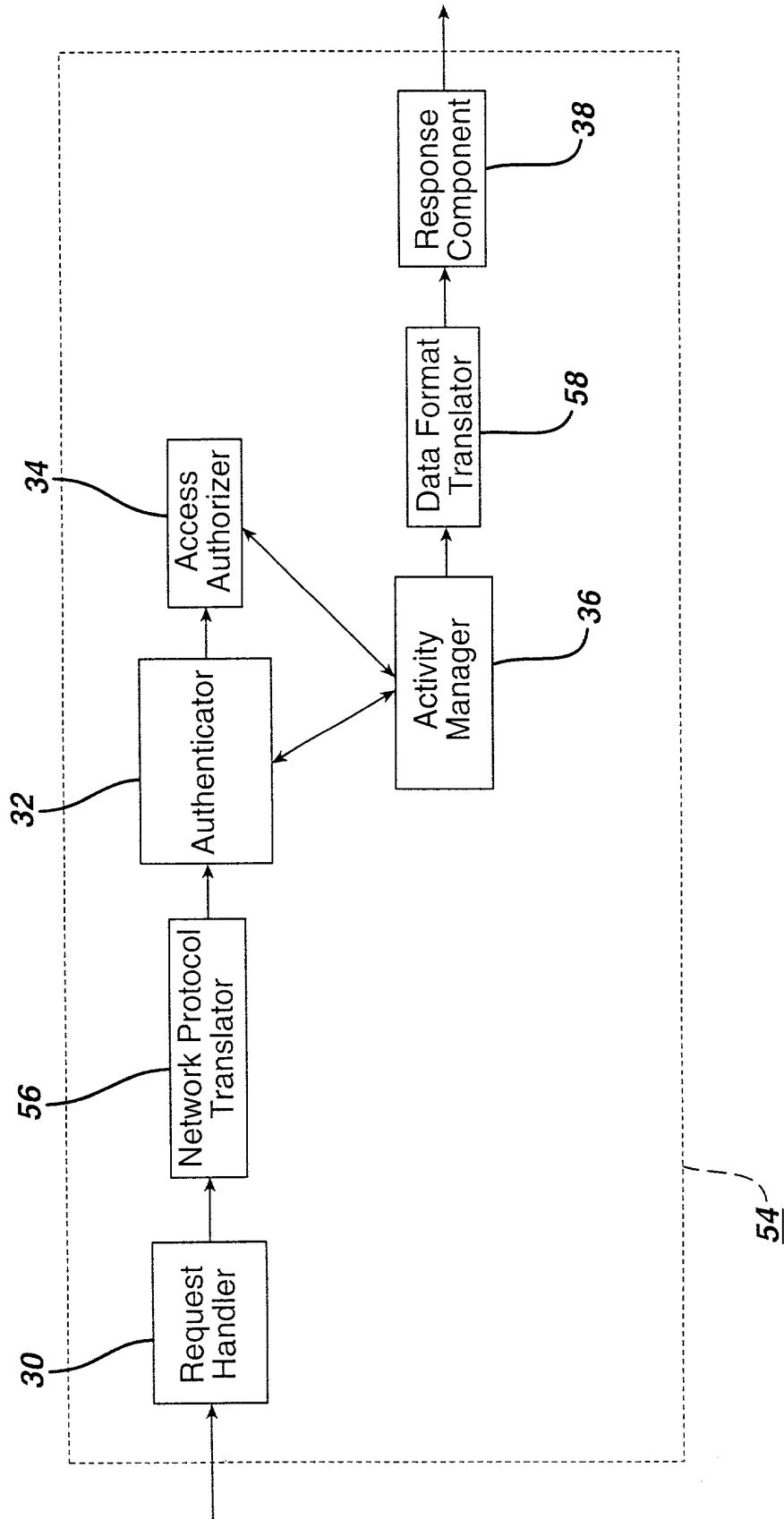


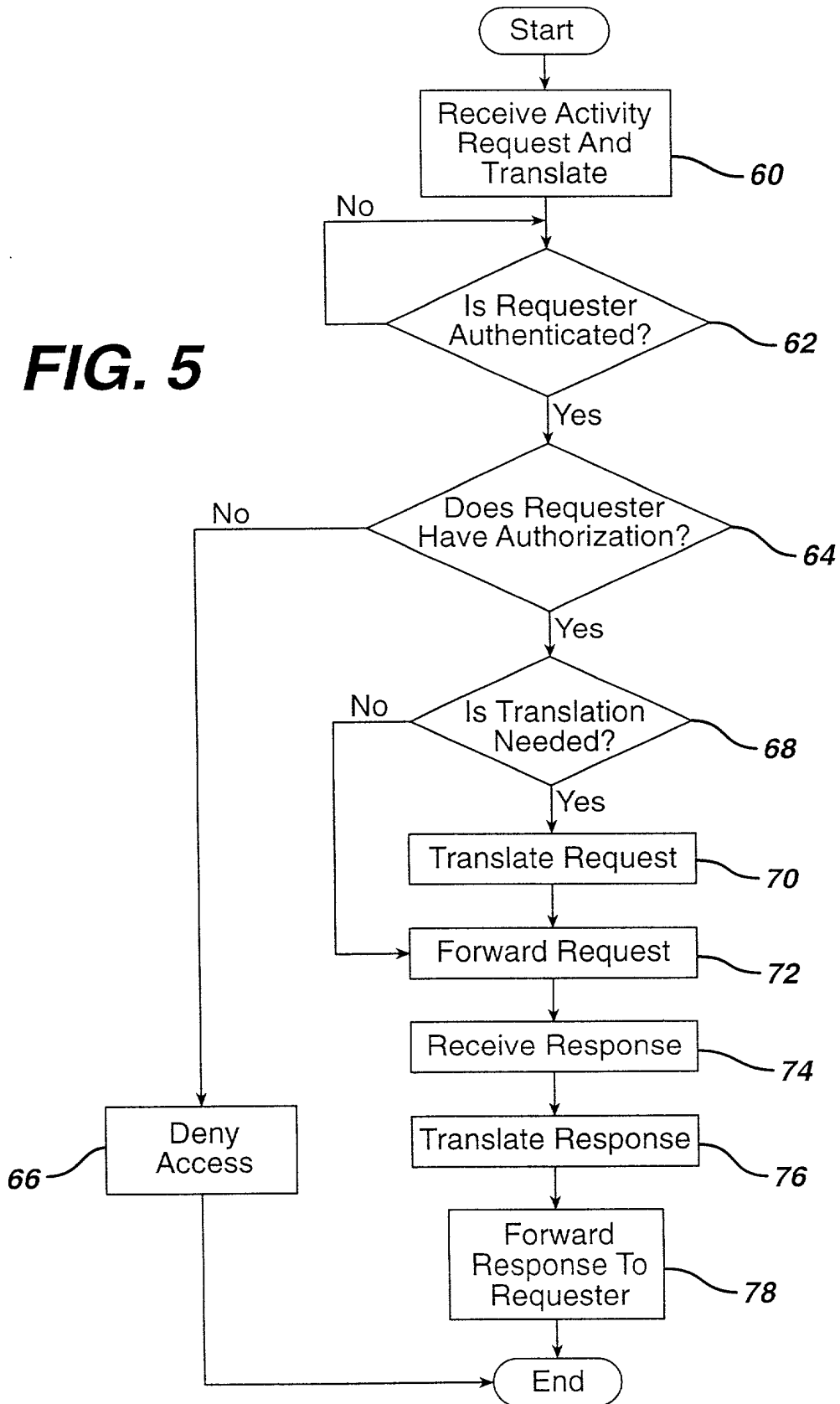
FIG. 5

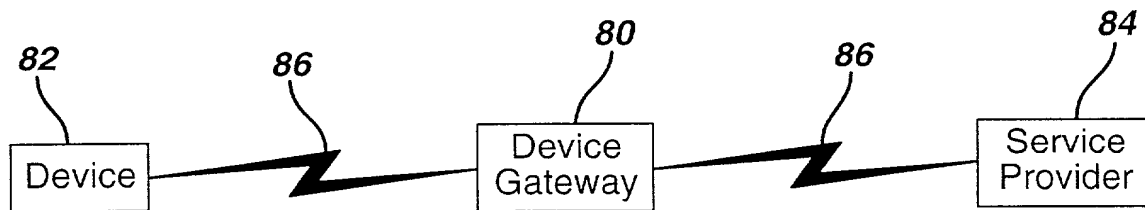
FIG. 6

FIG. 7

